

# IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

APPLICANT(S):

Card et al.

SERIAL NO.:

10/621,532

GROUP NO.:

Not yet assigned

**FÍLING DATE:** 

July 17, 2003

**EXAMINER:** 

Not yet assigned

TITLE:

Control of Complex Manufacturing Processes Using Continuous

Process Data

### CERTIFICATE OF FIRST CLASS MAILING UNDER 37 C.F.R. 1.8

I hereby certify that this correspondence, and any document(s) referred to as enclosed herein, is/are being deposited with the United States Postal Service as first class mail, postage prepaid, in an envelope addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on this **3** day of September, 2003 Men Boucher

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Sir:

Submitted herewith is/are:

Transmittal Form (1 page); Information Disclosure Statement (3 pages); PTO Form 1449 (2 pages); copies of cited references (A-A5, B1-B2, C1-C12); and a return-receipt postcard.

IPE					10/01/	222		
011	4		Application Serial	Number	10/621,5			
OCT 0 3 2003 H			Filing Date		July 17, 2003			
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TRAI	RANSMITTA:	L	Examiner Name		·	assigned		
	FORM		Attorney Docket	No.	IBX-00			
			Patent No.		l	plicable		
			Issue Date		plicable			
ENCLOSURES (check all that apply)								
Fee	Transmittal Form		Copy of Notice to Parts of Application	File Missing		Notice of Appeal to Board of Patent Appeals and Interferences		
	Check Attached Copy of Fee		Formal Drawing(s)	į		Appeal Brief (in triplicate)		
	Transmittal Form  Amendment/Response		Request For Conti Examination (RCI	nued E)		Status Inquiry		
_	☐ Preliminary ☐ After Final		Transmittal	·		Return Receipt Postcard		
	Affidavits/declaration(s) Letter to Official Draftsperson		Power of Attorney (Revocation of Pr	ior Powers)	☒	Certificate of First Class Mailing under 37 C.F.R. 1.8		
	including Drawings [Total Sheets]		Terminal Disclain	mer		Certificate of Facsimile Transmission under 37 C.F.R. 1.8		
	Petition for Extension of Time		Executed Declars of Attorney for U Patent Application	Itility or Design		Additional Enclosure(s) (please identify below)		
⋈	Information Disclosure		Small Entity Sta	tement				
	Statement Form PTO-1449 Copies of IDS Citations		CD(s) for large program	table or computer				
	Certified Copy of Priority		Amendment Aff	ter Allowance				
	Document(s)		Request for Certificate of					
	Sequence Listing submission Paper Copy/CD Computer Readable Copy Statement verifying identity of above		Correction  Certificate of Correction (in duplicate)					
CORRESPONDENCE ADDRESS  SIGNATURE BLOCK Respectfully submitted,								
	cet all correspondence to: Patent Testa, High 9 125 H Bosto	& Thibeault, LLP wer t	Date: September Reg. No. 33,497 Tel. No.: (617) Fax No.: (617)	, 310-8108	Steven J. Frank Attorney for Applicant(s) Testa, Hurwitz & Thibeault, LLP			
Fax No.: (			248-7100	1 ax 140 (017)		High Street Tower 125 High Street Boston, MA 02110		



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Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

#### INFORMATION DISCLOSURE STATEMENT

Sir:

In accordance with the provisions of 37 C.F.R. 1.97 and 1.98, Applicants hereby make of record the patents and publications listed on the accompanying Form PTO-1449, and other information contained herein, for consideration by the Examiner in connection with the examination of the above-identified patent application. Copies of the patents and publications are enclosed.

#### REMARKS

In accordance with the provisions of 37 C.F.R. 1.97, this statement is being filed (CHECK ONE):

$\boxtimes$	(1)	within three (3) months of the filing date of a national application other than a continued prosecution application under 37 C.F.R. 1.53(d), or within three (3) months of the date of entry of the national stage as set forth in 37 C.F.R. 1.491 in an international application, or before the mailing of the first Office action on the merits, or before the mailing of a first Office action after the filing of a request for continued examination under 37 C.F.R. 1.114; or
	(2)	after the period defined in (1) but before the mailing date of a <b>final action</b> or a <b>notice</b> of allowance under 37 C.F.R. 1.311, and
		the requisite Statement is below, OR
		the requisite fee under 37 C.F.R. 1.17(p), namely \$180.00, is included herein, or

Information Serial No. 1 Page 2 of 3	Disclosu 0/621,53	2
	(3)	after the mailing date of a final action or notice of allowance but before the payment of the issue fee, AND
		the requisite Statement is below, AND
		the requisite petition fee under 37 C.F.R. 1.17(p), namely \$180.00 is included herein.
It is PTO-144	respect	tfully requested that each of the patents and publications listed on the attached Form other information contained herein, be made of record in this application.
		STATEMENT
[check tl	he app	d under 37 C.F.R. 1.97(e), Applicant(s), through the undersigned, hereby state either that ropriate space only if either (2) or (3) is checked on the previous page and the
Stateme	nt is re	equired]:
1		<ol> <li>Each item of information contained in the Information Disclosure Statement was first cited in any communication from a foreign patent office in a counterpart foreign application not more than three months prior to the filing of the Information Disclosure Statement; or</li> </ol>
		No item of information contained in the Information Disclosure Statement was cited in a communication from a foreign patent office in a counterpart foreign application, and, to the knowledge of the person signing this Statement after making reasonable inquiry, no item of information contained in the Information Disclosure Statement was known to any individual designated in 37 C.F.R. 1.56(c) more than three months prior to the filing of the Information Disclosure Statement.
Ir applica	additions, p	on, Applicant wishes to inform the Examiner about the following co-pending patent publications, issued patents, and Office Actions issued therein:

- U.S. Serial No. 10/243,963, filed on September 13, 2002, by Card et al. (IBX-001);
- U.S. Serial No. 10/244,154, filed on September 13, 2002, by Reitman et al. (IBX-002);
- U.S. Serial No. 10/645,209, filed on August 21, 2003, by Chan et al. (IBX-004); and
- U.S. Serial No. 10/656,607, filed on September 5, 2003, by Card et al. (IBX-008).

Information Disclosure Statement Serial No. 10/621,532 Page 3 of 3

Date: September <u>30</u>, 2003 Reg. No. 33,497

Tel. No.: (617) 310-8108 Fax No.: (617) 248-7100

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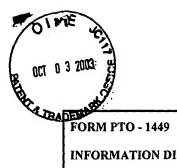
Respectfully submitted,

Steven J. Frank

Attorney for the Applicants Testa, Hurwitz, & Thibeault, LLP

High Street Tower

125 High Street Boston, Massachusetts 02110



INFORMATION DISCLOSURE STATEMENT

ATTORNEY DOCKET NO.: IBX-005

APPLICANT(S): Card et al.

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		<del> </del>	U.S.	. PATENT						- WOSIETION
EXAM. INIT.	DOCUMENT DATE NAME CLASS SUB FILING DATE NUMBER CLASS APPROPRIATE									
	Al	5,467,883	11/21/95	Frye et al.			216	60	11/27/93	
	A2	5,559,690	9/24/96	Keeler et	Keeler et al.			164	9/16/94	
	A3	5,654,903	8/5/97	Reitman e	Reitman et al.			551.01	11/7/95	
	A4	5,740,033	4/14/98	Wassick et al.			364	149	10/13/92	
	A5	6,268,226	7/31/01	Angell et al.			438	16	6/30/99	
	·		FOREI	GN PATE	NT DOCL	IMENTS	3		·	
EXAM. DOCUMENT DATE COUNTRY CLASS SUB CLASS		FILING DATE	ABSTRACT ENGLISH ONLY LANG (Y/N)		1					
	BI	WO 01/57605	8/9/01	wo	G05B	13/04	1/11/01	N		Y
	B2	DE196 37 917 A1	3/19/98	DE	G05B	13/04	9/17/96	Y		
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EXAM. INIT.	ОТН	ER DOCUMENTS	: (Including	g Author, Ti	tle, Date, Re	elevant Pa	ges, Place o	f Publicat	ion)	
1	C1 Card et al., "Dynamic Neural Control for Plasma Etch Process," <u>IEEE Transactions on Neural Networks</u> , (1997).									
/	C2	Dillon et al., "Guest Editorial Everyday Applications of Neural Networks," <u>IEEE Transactions on Neural Networks</u> , 8:4 (July 1997).								
1	C3	C3 Hatzipantelis et al., "Comparing Hidden Markov Models with Artificial Neural Network Architectures for Condition Monitoring Applications," <u>Artificial Neural Networks</u> , 26-28, Conference Publication No. 409 (June 1995).								
/	C4	C4 Kim et al., "Intelligent Control of Via Formation by Photosensitive BCB for MCM-L/D Applications," <u>IEEE Transactions on Semiconductor Manufacturing</u> , 12:503 (1999).						cations," <u>IEEE</u>		
/	C5 Konstantopoulos et al., "Controllers with Diagnostic Capabilities. A Neural Network Implementation.  Journal of Intelligent and Robotic Systems," Department of Electrical Engineering, University of Notre  Dame, JN 12: 197-228 (1995).									

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Γ	13/	C6	Lada et al., "A Wavelet-Based Procedure for Process Fault Detection," (September 17, 2001).						
	<b>3</b> /	C7	Moyne, "AEC/APC Vision: A Research and Suppliers' Point of View," 3 <sup>rd</sup> Annual European AEC/APC Conference Proceedings (2002).						
		C8	Rietman et al., "A Study on $\Re^m \to \Re^1$ Maps: Application to a 0.16- $\mu$ m Via Etch Process Endpoint," IEEE (2000).						
f		C9	Rietman et al., "A System Model for Feedback Control and Analysis of Yield: A Multistep Process Model of Effective Gate Length, Poly Line Width, and IV Parameters", <u>IEEE</u> (2001).						
ŀ		C10	Rietman, "Neural Networks in Plama Processing," <u>Journal of Vacuum Science and Technology: Part B.</u> <u>IEEE Transactions on Semiconductor Manufacturing</u> , 14:1 (2001).						
t	1	C11	Smyth et. al., "Hidden Markov Models an Neural Networks for Fault detection in Dynamic Systems," California Institute of Technology (1993).						
ļ	/	C12	Zhang et al, "Control of Spatial Uniformity in Microelectronics Manufacturing: An Integrated Approach," Proceedings of AEC/APC (2000).						
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l	EXAMI	NER	DATE CONSIDERED						